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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Nakagawa, K.

Serial No.: 09/688,203

Group Art Unit: 2823

Filed: October 16, 2000

Examiner: Brewster, William

For: METHOD OF MANUFACTURING SEMICONDUCTOR MEMORY DEVICE

Commissioner for Patents  
Alexandria, VA 22313-1450

**REQUEST FOR CONSIDERATION OF  
INFORMATION DISCLOSURE STATEMENT**

Sir:

Applicant respectfully requests that the Information Disclosure Statement submitted and filed on June 11, 2003, be officially considered of record. Attached is a copy of the Form PTO-1449 filed on June 11, 2003. Applicant respectfully requests that the Examiner officially acknowledge the disclosed information and promptly return the initialed Form PTO-1449.

Dated: 10/23/03

Respectfully submitted,

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(Use several sheets if necessary)

**OCT 23 2003**

**NEC00P264-ks**

**09/688,203**

**Nakagawa**

**October 16, 2000**

2823

**U.S. PATENT DOCUMENTS**

[illegible]

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		08-148658	06/07/96	JAPAN			ABS	
		02-151073	06/11/90	JAPAN			ABS	
		10-326881	12/08/98	JAPAN			ABS	
		08-046159	02/16/96	JAPAN			ABS	
		11-233652	08/27/99	JAPAN			ABS	

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

Japanese Office Action dated March 12, 2003 with partial English translation.

**EXAMINER**

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.